

Inventor: Trung Tri Doan
Title: Variable Temperature Deposition Methods
Assignee: Micron Technology, Inc.
Docket No. MI22-1372



INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

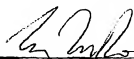
The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56.

Copies of documents listed on the Form PTO-1449 are enclosed.


No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 8/8/01

Inventor: 
Trung Tri Doan

Dated: 09-Aug-2001

Attorney: 
James E. Lake
Reg. No.: 44,854

EL844048295

2 Sheet 1 of 2

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1372	SERIAL NO. Unknown
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Trung Tri Doan	
				FILING DATE Herewith	GROUP Unassigned

U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,270,247	Sakuma et al.				
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
	AJ	EP 0 794 568	Europe	HD1L	21/768	Yes	No
	AK					X	
	AL						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)		
AM		Noah Precision, Inc., Internet website: http://www.noahprecision.com/tec.htm ; "Thermoelectric Temperature Control" 1998, pps. 1-2.
AN		Noah Precision, Inc., Internet website: http://www.noahprecision.com/cvd.htm ; "CVD" 1998, pps.1-2.
AO		Kiyoko et al., Patent Abstract Application No. 04-024917 (JP 9224917, 09/1993), "Semiconductor Substrate and Its Manufacture."
AP		Ritala, et al., "Atomic Layer Epitaxy - A Valuable Tool for Nanotechnology?" Nanotechnology, Vol. 10, No. 1, pps. 19-24, March 1999.
AQ		George, et al., "Surface Chemistry for Atomic Layer Growth", Journal of Physical Chemistry, Vol. 100, No. 31, pps. 13121-13131, August 1, 1996.
AR		Suntola, "Atomic Layer Epitaxy", Handbook of Crystal Growth, Vol. 3, Chapter 14, pps. 602-663, 1994.
AS		Vernon, S.M., "Low-cost, high-efficiency solar cells utilizing GaAs-on-Si technology," Dialog Abstract of Report No. NREL/TP-451-5353; 04/1993.

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1050 U.S. PTO
 09/19/2000
 08/09/01

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U.S. PATENT DOCUMENTS						
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	09/643,004	Mercaldi			08/21/2000
	AB	09/619,449	Derderian			07/19/2000
	AE	09/652,533	Derderian			08/31/2000
	AD					
	AE					
	AE					
	AB					
	AH					
	AI					
	AJ					
	AB					
	AB					
FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Subclass	Translation
						Yes No
	AM					
	AN					
	AO					
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)						
	AP					
	AQ					
	AR					
EXAMINER			DATE CONSIDERED			
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